

# **Product Preview**

1 December 2017

## **CMX602B**

## Calling Line Identifier plus Call Waiting CLI and CIDCW

The CMX602B is a CMOS integrated circuit for the reception of physical layer signals used in BT's Calling Line Identification Service (CLIP), Bellcore's Calling Identity Delivery system (CID), the Cable Communications Association's Caller Display Services (CDS) and other similar evolving services. It also meets requirements of Caller Identity with Call Waiting services (CIDCW).

#### **Features**

- CLI and CIDCW detector with 'zero power' ring or line reversal detector
- V.23/Bell 202 FSK demodulator with data retiming facility
- Dual tone alert detector
- On and off-hook operation
- Meets Belcore, British Telecom and ETSI 'CLI and CIDCW' specifications
- μC interrupt/wake-up output to minimise system operating power

### **Applications**

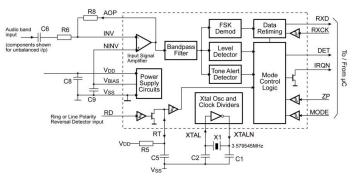
- Calling line ID and waiting systems
- Adjunct boxes
- Computer security systems
- Call monitoring
- Computer billing systems
- Telephone based utility metering
- No-ring calling

### **Supply Requirement**

2.7 to 5.5V

The device includes a 'zero-power' ring or line polarity reversal detector, a dual-tone (2130Hz plus 2750Hz) Tone Alert Signal detector and a 1200-baud FSK V23/Bell 202 compatible asynchronous data demodulator with a data retiming circuit, removing the need for a UART in the associated  $\mu\text{Controller}.$ 

It is suitable for 'on hook' use in systems to BT specifications SIN227 and SIN 242, Bellcore GR-30-CORE and SR-TSV-002476, CCA TW/P7 and E/312, ETSI 200-659 (parts 1 and 2), ETSI 300 778 (parts 1 and 2) and Mercury Communications MNR 19.



CMX602B Block Diagram



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