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# ON Semiconductor®

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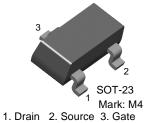
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### **BSR56**

# N-Channel JFET Low-Frequency Low-Noise Amplifier

 This device is designed for low-power chopper or switching application sourced from process 51



### Absolute Maximum Ratings $\rm T_{C} = 25^{\circ}C$ unless otherwise noted

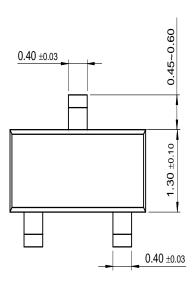
Symbol	Parameter	Value	Units	
$V_{DGO}$	Drain-Gate Voltage	40	V	
$V_{GSO}$	Gate-Source Voltage	- 40	V	
I <sub>GF</sub>	Forward Gate Current	50	mA	
P <sub>tot</sub>	Total Power Dissipation up to T <sub>amb</sub> =40°C	250	mW	
T <sub>STG</sub>	Storage Temperature Range	- 55 ~ 150	°C	
TJ	Junction Temperature	150	°C	

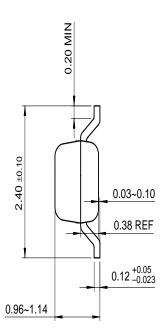
### **Electrical Characteristics** $T_C=25$ °C unless otherwise noted

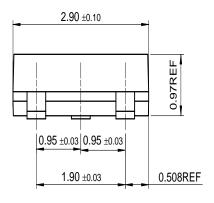
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>GSS</sub>	Gate-Source Voltage	$V_{DS} = 0V, I_{C} = 1\mu A$	40			V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = 20V			1	nA
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current	$V_{DS} = 15V, V_{GS} = 0V$	50			mA
V <sub>GS</sub> (off)	Gate-Source Cut-off Voltage	V <sub>DS</sub> = 15V, I <sub>D</sub> = 0.5nA	4		10	V
V <sub>DS</sub> (on)	Drain-Source On Voltage	$V_{GS} = 0V$ , $I_D = 20mA$			750	mV
r <sub>ds</sub> (on)	Drain-Source On Reverse	$V_{GS} = 0V, I_{D} = 0$			25	Ω
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V			5	pF
t <sub>d</sub>	Delay Time	$V_{DD} = 10V, V_{GS}(on) = 0V$			6	nS
t <sub>r</sub>	Rise Time	$I_D = 20 \text{mA}, V_{GS}(\text{off}) = 10 \text{V}$			3	nS
t <sub>off</sub>	Turn-off Time				25	nS

# **Package Dimensions**

## **SOT-23**







Dimensions in Millimeters

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CoolFET™	FASTr™	MicroFET™	PowerTrench <sup>®</sup>	SuperSOT™-6
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E <sup>2</sup> CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	$I^2C^{TM}$	$OCX^{TM}$	RapidConfigure™	UHC™
Across the board.	Around the world.™	OCXPro™	RapidConnect™	UltraFET <sup>®</sup>
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